

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S31	2340	349/149-152.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/04/04 09:21
S32	2	("20010043178").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2008/07/14 18:30
S33	63	"345".clas. and ((data signal) adj (driver ac)) same (timing clock\$5) near5 delay	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/14 18:33
S34	1	((data signal) adj (drivers ic ics)) same (timing clock \$5) near5 delay with (difference)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2008/07/16 10:57
S35	13	("5089812" "5365284" "5512915" "5579027"). PN. OR ("5808596"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/16 11:00
S36	20	(dual near5 bank near5 (driv\$3 ic)) same (timing clock\$5)	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/16 11:11
S37	1446	(dual near5 (driv\$3 ic)) same (timing clock\$5)	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/16 11:42
S38	51	(dual near5 (driv\$3 ic)) same (timing clock\$5) with delay	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/16 11:42
S39	849	(resist\$5 with capacit\$5 with delay with (equivalen \$5 differ\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/17 13:16
S40	175	(resist\$5 with capacit\$5 with delay with (equivalen \$5 differ\$5)) same (driv\$3 ic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/17 13:16
S41	56	(resist\$5 with capacit\$5 with delay with (equivalen \$5 differ\$5)) same (driv\$3 ic chip circuit) and (display.ti,ab,clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/17 13:17
S42	8	((("5534892") or ("5200847") or ("7196697") or ("5150238")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2008/07/17 14:31

S43	4	((("20030080932") or ("20030043097")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2008/07/17 14:34
S44	5	(resist\$5 with capacit\$5 with delay with (equivalen\$5 differ\$5)) same (driv\$3 ic chip circuit) same ns and (display.ti,ab,clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/17 17:09
S45	5	(resist\$5 with capacit\$5 with delay with (equivalen\$5 differ\$5)) same (driv\$3 ic chip circuit) same (ns nanosecond) and (display.ti,ab,clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/17 18:15
S46	48	(dual near5 (driv\$3 ic)) same (timing clock\$5) with (nanosecond ns)	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/17 18:17
S47	36	(resist\$5 same capacit\$5 same delay same (equivalen\$5 differ\$5)) same (driv\$3 ic chip circuit) same (microsecond "mu.m" nanosecond ns) and (display.ti,ab,clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/17 18:20
S48	36	(resist\$5 same capacit\$5 same delay same (equivalen\$5 differ\$5)) same (driv\$3 ic chip circuit) same (microsecond "mu.s" nanosecond ns) and (display.ti,ab,clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2008/07/17 18:24

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